

Senior ASIC AMS Verification Engineer (m/f/d) - A OT AESS

หน้าที่ความรับผิดชอบในงานของคุณ

As **Senior ASIC Analog/Mixed-Signal Verification Engineer** you create models and verification tests of an ASIC in our Advanced Electronics & Semiconductor Solutions (AESS) department in Timisoara.

You have the opportunity to work on AUMOVIO's diverse innovative automotive applications and find optimized solutions for their specific needs.

The stringent automotive requirements are to be met during verification process.

Your tasks include

- Develop the mixed-signal verification strategy and its related test bench
- Create models for analog blocks to be used in mixed-signal verification
- Build block and system-level test benches and verification environments using Verilog/Verilog-A/SystemVerilog
- Construct support utilities for verification automation, test bench automation, and other verification enhancements to improve productivity
- Mentor the team and provide technical support for modelling and verification activities
- Support the development of verification test plans, and verification activities
- Contribute to verification infrastructure development for complex ASICs
- Formulate test plans based on functional requirements, applicable standards requirements
- Responsible for the definition, development, and execution of self-checking tests
- Contribute to the post-silicon validation of ASICs in the lab, writing tests and scripts

โปรไฟล์ของคุณ

- Master of Science / PhD in Electrical Engineering / Microelectronics / Physics or similar
- 5+ years of relevant verification experience in analog mixed-signal circuits
- Understanding of design of analog circuit
- Excellent programming skills on Verilog, SystemVerilog, VerilogAMS and Verilog-A
- Experience in script programming(Perl/Python/Makefile/Shell and) would be a plus



รหัสตำแหน่งงาน

REF9882H

สาขางาน

งานวิศวกรรม

ที่ตั้ง

Timișoara

ระดับความเป็นผู้นำ

Leading Self

นิติบุคคล

**AUMOVIO TECHNOLOGIES
ROMANIA S.R.L.**

- Testbench development for the verification of Mixed-Signal blocks using SystemVerilog
- Familiarity with best practice chip-level verification techniques and languages
- Experience with CAD Tools (ideally, Cadence AMS Simulation environment)
- Familiar with usage and creation of interface elements for mixed signal simulation
- Very good English language skills written and spoken
- Planning and organizing skills, ability to take initiative and to communicate effectively, eagerness to grow and develop

ข้อเสนอของเรา

Ready to take your career to the next level? The future of mobility isn't just anyone's job. Make it yours! **Join AUMOVIO. Own What's Next.**

เกี่ยวกับเรา

At AUMOVIO, we own what we do: Developing and producing hardware, software, and services that shape the future of mobility. But what truly sets us apart are our people. People who thrive in our winning culture, driven by bold ideas. People who take a leap and create meaningful difference—together. Because we believe that the future belongs to those who own it. That's why we foster a culture where you own every step of the way.

Own your career, your growth, your skills, and your impact. Join us and own what's next.

Advanced Electronics & Semiconductor Solutions (AESS) organization has been established in AUMOVIO to develop and manufacture semiconductors in a fabless model to fulfilling strategic internal semiconductor demands.