

ASIC Layout Engineer (m/f/d) - A OT AESS

담당 업무

As **ASIC Layout Engineer** (m/f/d) you generate the block and Chip layouts based on analog circuits / schematics in our Advanced Electronics & Semiconductor Solutions (AESS) department in Timisoara. The layout engineer chooses appropriate methods to reach robust DRC compliant layout with efficient area utilization. Layout engineer works very closely with the analog circuit designer.

Tasks

- Creating and integrating analog layouts on block and top level
- Floor planning, pin placement, parasitic extraction
- Support design engineer to reach speed, area, power, DFM & ESD/EMV requirements
- DRC, LVS & Tape out support

지원자 프로필

- Master of Science / PhD in Electrical Engineering / Microelectronics / Physics or similar
- Bachelor degree in electronics engineering
- +2 years of layout design experience
- Good knowledge in area of analog circuit layout
- Nice to have experience in doing layouts in BCD technology (Technologies: 65 - 180nm)
- Good knowledge in the area of deep sub micron effects (parasitics, coupling, matching etc.)
- Basic understanding of layout tools (design, DRC, LVS, Extraction)
- Basic knowledge of analog circuit design

Languages

- Fluent English

Capabilities

- Must be a team player with good communication and negotiation skills
- Customer focus (anticipates and understands the customers' needs)
- Motivation and inspiration (operates with integrity and demonstrates dynamism)
- Taking initiative

채우 조건

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Leading Self

법률 고지
**AUMOVIO TECHNOLOGIES
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기업 소개

At AUMOVIO, we own what we do: Developing and producing hardware, software, and services that shape the future of mobility. But what truly sets us apart are our people. People who thrive in our winning culture, driven by bold ideas. People who take a leap and create meaningful difference—together. Because we believe that the future belongs to those who own it. That's why we foster a culture where you own every step of the way.

Own your career, your growth, your skills, and your impact. Join us and own what's next.

Advanced Electronics & Semiconductor Solutions (AESS) organization has been established in AUMOVIO to develop and manufacture semiconductors in a fabless model to fulfilling strategic internal semiconductor demands.