

ASIC Analog Layout Engineer (m/f/d)

Jūsų užduotys

As an ASIC Layout Engineer (m/f/d), you will be responsible for the top-level integration of analog and mixed-signal ICs. You will drive the layout development process, ensuring robust, DRC-compliant designs with optimal area utilization. You will collaborate closely with analog circuit designers and other layout engineers to deliver high-quality silicon.

Key Responsibilities:

- Responsibility for top-level layout integration of analog/mixed-signal chips (analog on top)
- Defining toplevel floorplan, padframe design and bonding diagrams in coordination with other domains
- Coordinating with other layouters and drive floorplans according to the chip top level
- Generating and integrating analog layouts at block and chip level
- Performing pin placement, parasitic extraction, and layout optimization
- Responsibility for compliance with DRC, LVS, and support tape-out activities
- Collaborating with design engineers to meet speed, area, power, DFM, ESD, and EMC requirements

Reikalavimai

- Academic degree in electrical engineering, microelectronics, physics or comparable qualification
- Longterm experience in analog layout design
- Proven experience in top-level layout integration
- Strong expertise in BCD technologies (65nm to 180nm)
- Deep understanding of deep submicron effects (parasitics, coupling, matching)
- Proficient in layout tools for design, DRC, LVS, and extraction
- Knowledge of analog circuit design
- Fluent English language skills (written and spoken), German language skills are a plus

Applications from severely handicapped people are welcome.

Mes siūlome

- Next to the interesting daily work the region around Regensburg is offering a high recreational value and quality of life.
- Profit from extensive benefits at the location such as different canteens and cafeterias, a continuing education and training, health management, physiotherapy and much more.
- We offer an attractive salary as well as the possibility of hybrid and cross-border mobile work.



Darbo ID
REF9871G

Darbo sritis
Inžinerija

Vieta
Regensburg

Lyderystės lygis
Leading Self

Kontaktinis asmuo
Sarah Schwarz

Juridinis asmuo
AUMOVIO Germany GmbH

- For an improved work-life balance we support you with various initiatives for a better compatibility of family and career such as flexible work time model, part-time option, sabbatical, etc.
- You can expect a highly motivated team as well as diverse topics and projects in the global environment of the automotive industry.
- We are promoting lifelong Learning combined with personal and professional development.
- You are looking for an individual working time model? We support part-time work.

You would like to learn more about our additional services? Click [here](#).

Ready to take your career to the next level? The future of mobility isn't just anyone's job. Make it yours! **Join AUMOVIO. Own What's Next.**

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At AUMOVIO, we own what we do: Developing and producing hardware, software, and services that shape the future of mobility. But what truly sets us apart are our people. People who thrive in our winning culture, driven by bold ideas. People who take a leap and create meaningful difference—together. Because we believe that the future belongs to those who own it. That's why we foster a culture where you own every step of the way.

Own your career, your growth, your skills, and your impact. Join us and own what's next.

Advanced Electronics & Semiconductor Solutions (AESS) organization has been established in AUMOVIO to develop and manufacture semiconductors in a fabless model to fulfilling strategic internal semiconductor demands.