

Senior ASIC Analog Design Engineer (m/f/d)

Jūsų užduotys

As an ASIC Analog Design Engineer (m/f/d), you will act as a technical authority for analog and mixed-signal high-voltage IC development. This role goes beyond block ownership and requires technical leadership across architecture, requirements breakdown, methodology, risk mitigation, and chip-level integration. You will make high-impact decisions for safety-critical automotive ASICs, guide complex trade-offs across disciplines, mentor engineers, and ensure critical know-how is applied consistently across projects. Your contribution directly influences product quality, execution, innovation, and the long-term competitiveness of the analog design organization.

Your Tasks:

- Acting as key technical authority for analog and mixed-signal ASIC development, guiding complex high-voltage automotive products from concept to qualification
- Owning chip-level architecture and design trade-offs across performance, robustness, safety, EMC/ESD, testability, cost, schedule, and manufacturability
- Leading complex analog block definition and implementation in BCD technologies, including SMPSSs, LDOs, gate drivers, charge pumps, sensor interfaces, and protections
- Translating system and customer requirements into robust chip- and block-level specifications, identifying risks and verification needs early
- Representing analog design in technical negotiations with internal customers, architecture, project management, verification, digital, layout, test, technology, and quality teams
- Driving analog-on-top and chip-level integration, ensuring consistency across schematics, verification, layout, constraints, and validation strategy
- Reviewing and approving critical design concepts, simulation strategies, robustness analyses, layout implementation, validation plans, and issue resolutions
- Improving analog design methodologies, reusable approaches, best practices, checklists, and knowledge assets across projects
- Mentoring engineers, transfer knowledge, raise design maturity, and strengthen long-term analog design capability
- Contributing to strategic technical decisions, project recovery, roadmaps, hiring, and competency development for analog design

Reikalavimai

- Academic degree in electrical engineering, microelectronics, physics or comparable qualification
- Expert level of experience in analog/mixed-signal high-voltage, high-power ASIC design in the automotive domain



Darbo ID
REF9867H

Darbo sritis
Inžinerija

Vieta
Regensburg

Lyderystės lygis
Leading Self

Kontaktinis asmuo
Sarah Schwarz

Juridinis asmuo
AUMOVIO Germany GmbH

- Longterm Experience with analog-on-top simulation environments and mixed-signal verification
- Strong Knowledge of BCD technologies
- Knowledge of functional safety and reliability engineering
- Proven track record of tape-outs and successful silicon validation
- Exposure to EMC/ESD design and robustness testing
- Strong understanding of analog circuit theory, and physical layout considerations
- Familiarity with automotive-grade design constraints and qualification processes
- Very good English language skills (written and spoken)

Applications from severely handicapped people are welcome.

Mes siúlome

- Next to the interesting daily work the region around Regensburg is offering a high recreational value and quality of life.
- Profit from extensive benefits at the location such as different canteens and cafeterias, a continuing education and training, health management, physiotherapy and much more.
- We offer an attractive salary as well as the possibility of hybrid and cross-border mobile work.
- For an improved work-life balance we support you with various initiatives for a better compatibility of family and career such as flexible work time model, part-time option, sabbatical, etc.
- You can expect a highly motivated team as well as diverse topics and projects in the global environment of the automotive industry.
- We are promoting lifelong Learning combined with personal and professional development.
- You are looking for an individual working time model? We support part-time work.

You would like to learn more about our additional services? Click [here](#).

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At AUMOVIO, we own what we do: Developing and producing hardware, software, and services that shape the future of mobility. But what truly sets us apart are our people. People who thrive in our winning culture, driven by bold ideas. People who take a leap and create meaningful difference—together. Because we believe that the future belongs to those who own it. That's why we foster a culture where you own every step of the way.

Own your career, your growth, your skills, and your impact. Join us and own what's next.

Advanced Electronics & Semiconductor Solutions (AESS) organization has been established in AUMOVIO to develop and manufacture semiconductors in a fabless model to fulfilling strategic internal semiconductor demands.