ASIC Validation and Test Engineer (m/f/d)

담당 업무

As a an ASIC Validation and Test Engineer, you will be responsible for the validation, characterization, electrical failure analysis and functional testing of analog, mixed-signal and digital ICs. You will collaborate closely with analog circuit designers and other application engineers to deliver high-quality test coverage and reports.

Key Responsibilities

- Develop bench test concepts based on requirement specifications.
- Develop bench test specifications based on test concepts.
- Set up and maintain test benches and lab instrumentation for characterization.
- Perform initial validation of ASICs based on bench test specifications.
- Develop bench characterization concepts for automated characterization.
- Perform characterization of ASICs using automated bench test concepts.
- Document test results for initial validation & characterization tests.
- Programming of test management environment for automated bench testing (LabVIEW, TestStand, Python).
- Define concepts for test boards and perform schematic entry of concepts.
- Analysis and problem solving for field returns

지원자 프로필

- Master's or Bachelor's degree in Electronics, Electrical Engineering, or a related field.
- 5+ years of hands-on experience in application electronics, integrated circuits or ASIC testing, bench-level characterization.
- Test- and Microcontroller-programming knowledge.
- Knowledge of schematic entry.
- Experience in project-based work and cross-functional collaboration.
- Knowledge of analog and digital circuits.
- Strong problem-solving and analytical abilities.
- Excellent communication and customer-facing skills.
- Ability to work independently and in team environments.
- Detail-oriented with a proactive mindset.
- Good English language skills, written and spoken

처우 조건

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- Dedicated Programs and Conferences;
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- Internal development communities (Experts, Agile Community of Practice, Artificial Intelligence etc).

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