

## Design for Test Engineer (m/f/d) - REF84987L

### Jūsų užduotys

As DFT engineer (m/f/diverse) for Continental ASICs you define and implement DFT (Design for Test) and BIST (Built-In Self-Test) concepts, you also develop DFT specifications and driving DFT architecture and methods for designs as well as IPs to maximize test coverage while minimizing costs.

You have the opportunity to work on Continental's diverse innovative automotive applications and find optimized solutions for their specific needs. Special care must be taken to meet the stringent automotive requirements.

Your tasks will focus on the following activities:

- Define and implement DFT (Design for Test) and BIST (Built-In Self-Test) concepts, as well as IPs to maximize test coverage while minimizing costs
- Define and develop scripts for automatic scan insertion, execute scan insertion and ATPG (Automatic Test Pattern Generation)
- Perform test coverage analysis and improvement
- Lead and manage SOC Design for Test efforts for complex projects, ensuring the successful execution coverage, manufacturability, and quality plans
- Develop full chip and block level DFT implementation from the Specifications and product coverage, quality, and manufacturability goals
- Collaboratively create DFT methodology innovations & enhancements for Continental
- Work in close cooperation with the two main interfaces chip design and semiconductor manufacturing as well as related areas
- Implement and verify testability features to our System-on-Chip designs
- Create and simulate test patterns for production testing
- Develop DFT specifications and drive flow and methodology enhancements
- Implement and validate DFT structures such as LBIST, MBIST, IP tests, Scan & compression etc.
- Give technical guidance to other DFT engineers, concept engineers, digital designers, physical implementation, and functional verification teams
- Verify, support and understanding of pattern delivery to the post-silicon test engineering teams. Delivering the full chip requirements for the Test Engineering Team. Understanding tester requirements and delivering the patterns in the formats that the tester team's needs
- Work with cross-functional IP and SOC teams to define strategy for DFT architecture, verification, and design flow automation
- Contribute throughout the whole development and give early feedback to the Concept, Architecture and Design Teams for possible issues for DFT



Darbo ID  
**REF84987L**

Darbo sritis  
**Inžinerija**

Lyderystės lygis  
**Leading Self**

Darbo laiko lankstumas  
**Hybrid Job**

Kontaktinis asmuo  
**Daniela Krebs**

Juridinis asmuo  
**Continental Automotive Technologies GmbH**

- Define, develop, improve DFT process flows and methodologies for continuous improvement
- Working closely with IC Design during test plan and DFT definition, pre-silicon verification (chip level verification) and post silicon validation
- Work closely with IP test development team, defining test schemes and implementation styles

## **Reikalavimai**

- Academic degree in Electrical Engineering/Microelectronics/Physics or comparable qualification
- Many years of relevant work experience
- Proven Expertise in DFT engineering, with a strong record of technical innovation
- Experience in developing DFT specifications and driving DFT architecture and methods for designs
- Experience in industrial standards and practices in DFT - including ATPG, JTAG, MBIST and trade-offs between test quality and test time
- Skilled in SystemVerilog RTL, TCL, Python, and Unix/Linux environments
- Familiarity with Siemens, Cadence, and Synopsys DFT tools
- Very good English language skills (both written and spoken)
- Team player with good communication and negotiation skills
- Customer focus (anticipates and understands the customers' needs)
- Motivation and inspiration (operates with integrity and demonstrates dynamism)

Applications from severely handicapped people are welcome.

## **Mes siūlome**

Are you ready to take your career to the next level and become part of something big? Apply now and become part of AUMOVIO to shape the mobility of the future together with us!

Ready to take your career to the next level and join us at the start of something extraordinary? Apply now to become a part of AUMOVIO and drive the future mobility together with us!

## **Apie mus**

Continental's Automotive group sector is expected to be listed as independent company "AUMOVIO" in September 2025. With ~93,000 employees worldwide and annual sales of ~€20 billion, we are entering an exciting new era.

AUMOVIO stands for highly developed electronic products and modern mobility solutions. In addition to its strong market position with innovative sensor solutions, displays, and technologically leading braking and comfort systems, AUMOVIO has significant expertise in software, architecture platforms and assistance systems for the rapidly growing future market of software-defined and autonomous vehicles. Our purpose is clear: to make future mobility safe, exciting, connected, and autonomous.