

IC Digital Design Engineer

工作职责

Internal

Digital Design Engineer

Responsibilities

- Architect, design, and verify complex digital front-end circuits and systems for FPGA and ASIC implementations, including defining digital design requirements, developing architectural specifications, and coding high-quality HDL modules that meet performance, reliability, and scalability targets.
- Execute all assigned tasks with strong focus on reusability, efficiency, and area-optimized design.
- Lead/participate in comprehensive design reviews, providing clear, constructive feedback to enhance design robustness, quality, and development efficiency.
- Apply mixed-signal understanding (is a strong plus) to ensure smooth integration of digital front-end architectures with analog and mixed-signal components.
- Proactively define optimal system and component-level concepts, driving digitization initiatives in close collaboration with engineers from all relevant disciplines.
- Collaborate with cross-functional engineering teams (eg. application and system engineers) to establish cost-effective solutions that meet functional, performance, and architectural objectives.
- Provide technical guidance and mentorship during reviews and technical discussions, contributing to a culture of engineering excellence.
- Continuously refine and enhance digital design methodologies, development workflows, and toolchains to increase productivity and design consistency.
- Create and maintain comprehensive digital design documentation, including requirements, architecture descriptions, design specifications, and block-level details.
- Document and/or review design processes, verification strategies, test plans, and results in compliance with industry standards and best practices.

Required Qualifications

- Master's or PhD in Electronics Engineering or a related technical field with 6+ years of relevant experience, or a Bachelor's degree with 8+



职位号码

REF79718I

所在地

Bengaluru

领导力级别

个人贡献者

法律实体名称

AUMOVIO India Private Limited

years of experience in digital design, SoC integration, or ASIC digital architecture.

Internal

- Strong proficiency in hardware description languages (HDLs) such as Verilog and SystemVerilog or VHDL.
- Proven track record of delivering complex digital design projects and collaborating effectively across multidisciplinary teams.
- Deep expertise in ASIC and FPGA design methodologies, including front-end architecture and implementation flows.
- Working knowledge of UVM is a strong plus.
- Experience with Design for Testability (DFT) and Design for Manufacturing (DFM) practices, is a plus.
- Strong understanding of the digital design flow, including synthesis, simulation, static timing analysis, and related front-end development processes.
- Hands-on experience with formal verification tools, such as Siemens Questa Formal and Synopsys Spyglass.
- Good understanding of CDC (Clock Domain Crossing), RDC (Reset Domain Crossing), and multi-power-domain design techniques.

职位要求

B.E/ BTech in E&C (Masters in VLSI recommended)

我们可以提供

Ready to take your career to the next level? The future of mobility isn't just anyone's job. Make it yours! **Join AUMOVIO. Own What's Next.**

关于我们

Since its spin-off in September 2025 AUMOVIO continues the business of the former Continental group sector Automotive as an independent company. The technology and electronics company offers a wide-ranging portfolio that makes mobility safe, exciting, connected, and autonomous. This includes sensor solutions, displays, braking and comfort systems as well as comprehensive expertise in software, architecture platforms, and assistance systems for software-defined vehicles. In the fiscal year 2024 the business areas, which now belong to AUMOVIO, generated sales of 19.6 billion Euro. The company is headquartered in Frankfurt, Germany and has about 87.000 employees in more than 100 locations worldwide.