

## Digital Design Engineer (m/f/d) - REF6693T

### 工作职责

- Architect, design, and verify complex digital front-end circuits and systems for FPGA and ASIC implementations, including defining digital design requirements, developing architectural specifications, and coding high-quality HDL modules that meet performance, reliability, and scalability targets
- Execute all assigned tasks with strong focus on reusability, efficiency, and area-optimized design
- Lead/participate in comprehensive design reviews, providing clear, constructive feedback to enhance design robustness, quality, and development efficiency
- Apply mixed-signal understanding (is a strong plus) to ensure smooth integration of digital front-end architectures with analog and mixed-signal components
- Proactively define optimal system and component-level concepts, driving digitization initiatives in close collaboration with engineers from all relevant disciplines
- Collaborate with cross-functional engineering teams (eg. application and system engineers) to establish cost-effective solutions that meet functional, performance, and architectural objectives
- Provide technical guidance and mentorship during reviews and technical discussions, contributing to a culture of engineering excellence
- Continuously refine and enhance digital design methodologies, development workflows, and toolchains to increase productivity and design consistency
- Create and maintain comprehensive digital design documentation, including requirements, architecture descriptions, design specifications, and block-level details
- Develop, execute, document and/or review design processes, verification strategies, test plans, and results in compliance with industry standards and best practices
- Build SystemVerilog/UVM-based testbenches and reusable verification components

### 职位要求

#### Required Skills & Qualifications

- Master's or PhD in Electronics Engineering or a related technical field with 4+ years of relevant experience, or a Bachelor's degree with 6+ years of experience in digital design, SoC integration, or ASIC digital architecture.
- Strong proficiency in hardware description languages (HDLs) such as Verilog and SystemVerilog or VHDL.
- Proven track record of delivering complex digital design projects and collaborating effectively across multidisciplinary teams.
- Deep expertise in ASIC and FPGA design methodologies, including



职位号码  
**REF6693T**

工作职能  
工程

所在地  
**Regensburg**

领导力级别  
个人贡献者

招聘专员  
**Daniela Krebs**

法律实体名称  
**AUMOVIO Germany GmbH**

front-end architecture and implementation flows.

- Working knowledge of UVM.
- Strong understanding of the digital design flow, including synthesis, simulation, static timing analysis, and related front-end development processes.
- Hands-on experience with formal verification tools, such as Siemens Questa Formal and Synopsys Spyglass.
- Good understanding of CDC (Clock Domain Crossing), RDC (Reset Domain Crossing), and multi-power-domain design techniques.

### Preferred Qualifications

- Experience in C IPs eg. ARM or RISC-V is a strong plus
- Proficiency with verification simulators (e.g., VCS, Questa, Xcelium)
- Experience with Design for Testability (DFT) and Design for Manufacturing (DFM) practices
- Knowledge of Formal Verification methodologies and tools (e.g., JasperGold, VC Formal).
- Strong scripting experience for automation (Python, Perl, Shell, Tcl).
- Experience with SoC-level verification, emulation, or FPGA prototyping.
- Exposure to low-power/UPF verification flows.

Applications from severely handicapped people are welcome.

我们可以提供

- Next to the interesting daily work, the region around Regensburg is offering a high recreational value and quality of life.
- Profit from extensive benefits at the location such as different canteens and cafeterias, a continuing education and training, health management, physiotherapy and much more.
- We offer an attractive salary as well as the possibility of hybrid and cross-border mobile work.
- For an improved work-life balance we support you with various initiatives for a better compatibility of family and career such as flexible work time model, part-time option, sabbatical, etc.
- You can expect a highly motivated team, as well as diverse topics and projects in the global environment of the automotive industry.
- We are promoting lifelong learning combined with personal and professional development.
- You are looking for an individual working time model? We support part-time work.

Ready to take your career to the next level? The future of mobility isn't just anyone's job. Make it yours! **Join AUMOVIO. Own What's Next.**

Ready to take your career to the next level? The future of mobility isn't just anyone's job. Make it yours! **Join AUMOVIO. Own What's Next.**

关于我们

Since its spin-off in September 2025 AUMOVIO continues the business of the former Continental group sector Automotive as an independent company. The technology and electronics company offers a wide-ranging portfolio that makes mobility safe, exciting, connected, and

autonomous. This includes sensor solutions, displays, braking and comfort systems as well as comprehensive expertise in software, architecture platforms, and assistance systems for software-defined vehicles. In the fiscal year 2024 the business areas, which now belong to AUMOVIO, generated sales of 19.6 billion Euro. The company is headquartered in Frankfurt, Germany and has about 87.000 employees in more than 100 locations worldwide.