

ASIC Analog Layout Engineer (m/f/d) - REF5390S

工作职责

As an ASIC Layout Engineer (m/f/diverse) for AESS (Advanced Electronics & Semiconductor Solutions), you will be responsible for the top-level integration of analog and mixed-signal ICs. You will drive the layout development process, ensuring robust, DRC-compliant designs with optimal area utilization. You will collaborate closely with analog circuit designers and other layout engineers to deliver high-quality silicon.

Key Responsibilities

- Drive the top-level layout integration of analog/mixed-signal chips (analog on top)
- Define toplevel floorplan, padframe design and bonding diagrams in coordination with other domains
- Coordinate with other layouters to define floorplans according to the chip top level
- Generate and integrate analog layouts at block and chip level
- Perform pin placement, parasitic extraction, and layout optimization
- Ensure compliance with DRC, LVS, and support tape-out activities
- Collaborate with design engineers to meet speed, area, power, DFM, ESD, and EMC requirements

职位要求

- Academic degree in Electronics Engineering or related field
- Extensive professional experience in analog layout design
- Proven experience in top-level layout integration
- Strong expertise in BCD technologies (65nm to 180nm)
- Deep understanding of deep submicron effects (parasitics, coupling, matching)
- Proficient in layout tools for design, DRC, LVS, and extraction
- Knowledge of analog circuit design
- Fluent English language skills (both written and spoken), German language skills are a plus

Applications from severely handicapped people are welcome.

我们可以提供

- Next to the interesting daily work, the region around Regensburg is offering a high recreational value and quality of life.
- Profit from extensive benefits at the location such as different canteens and cafeterias, a continuing education and training, health management, physiotherapy and much more.
- We offer an attractive salary as well as the possibility of hybrid and cross-border mobile work.
- For an improved work-life balance we support you with various



职位号码
REF5390S

工作职能
工程

所在地
Regensburg

领导力级别
个人贡献者

招聘专员
Daniela Krebs

法律实体名称
AUMOVIO Germany GmbH

initiatives for a better compatibility of family and career such as flexible work time model, part-time option, sabbatical, etc.

- You can expect a highly motivated team, as well as diverse topics and projects in the global environment of the automotive industry.
- We are promoting lifelong learning combined with personal and professional development.
- You are looking for an individual working time model? We support part-time work.

Ready to take your career to the next level? The future of mobility isn't just anyone's job. Make it yours! **Join AUMOVIO. Own What's Next.**

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关于我们

At AUMOVIO, we own what we do: Developing and producing hardware, software, and services that shape the future of mobility. But what truly sets us apart are our people. People who thrive in our winning culture, driven by bold ideas. People who take a leap and create meaningful difference—together. Because we believe that the future belongs to those who own it. That's why we foster a culture where you own every step of the way.

Own your career, your growth, your skills, and your impact. Join us and own what's next.

Advanced Electronics & Semiconductor Solutions (AESS) organization has been established in AUMOVIO to develop and manufacture semiconductors in a fabless model to fulfilling strategic internal semiconductor demands.