

## (Senior) ASIC Analog Design Engineer (m/f/d) - REF5389A

### 담당 업무

As an (Senior) ASIC Analog Design Engineer (m/f/diverse) for AESS (Advanced Electronics & Semiconductor Solutions), you will lead the development of analog and mixed-signal high-voltage integrated circuits for automotive applications, such as sensor interfaces and smart power devices. You will be responsible for complex transistor level analog designs, analog-on-top integration, requirements breakdown, mentoring junior engineers and chip top-level integration. Your work will directly contribute to the safety, efficiency, and innovation of next-generation automotive systems.

### Tasks:

- Act as a key technical interface, coordinating with multidisciplinary teams—including system architects, digital designers, verification engineers, layout specialists, and test engineers—to ensure optimal design trade-offs and successful project execution.
- Define and implement analog blocks in BCD technologies, such as SMPSs, LDOs, Gate Drivers, Charge Pumps
- Perform requirements breakdown from system-level specifications to block-level design targets
- Discuss, negotiate and investigate chip requirements with internal customers
- Collaborate with digital design, layout, verification, and test teams to ensure seamless integration
- Perform chip-top level integration

### 지원자 프로필

- Academic degree in electrical engineering, microelectronics, physics or comparable qualification
- Senior-level of experience in analog/mixed-signal high-voltage, high-power ASIC design in the automotive domain
- Good understanding of BCD technologies
- Proven track record of tape-outs and successful silicon validation
- Knowledge of functional safety and reliability engineering
- Exposure to EMC/ESD design and robustness testing
- Strong understanding of analog circuit theory, and physical layout considerations
- Experience with analog-on-top simulation environments and mixed-signal verification
- Familiarity with automotive-grade design constraints and qualification processes
- Very good English language skills (both written and spoken), German language skills desirable
- Ability to take initiative and to communicate effectively
- Eagerness to grow and develop



직무-아이디

**REF5389A**

모집 분야

공학

지사

**Frankfurt am Main**

리더십 레벨

**Leading Self**

이름

**Daniela Krebs**

법률 고지

**AUMOVIO Germany GmbH**

Applications from severely handicapped people are welcome.

## 채우 조건

Ready to take your career to the next level? The future of mobility isn't just anyone's job. Make it yours! **Join AUMOVIO. Own What's Next.**

## 기업 소개

At AUMOVIO, we own what we do: Developing and producing hardware, software, and services that shape the future of mobility. But what truly sets us apart are our people. People who thrive in our winning culture, driven by bold ideas. People who take a leap and create meaningful difference—together. Because we believe that the future belongs to those who own it. That's why we foster a culture where you own every step of the way.

Own your career, your growth, your skills, and your impact. Join us and own what's next.

Advanced Electronics & Semiconductor Solutions (AESS) organization has been established in AUMOVIO to develop and manufacture semiconductors in a fabless model to fulfilling strategic internal semiconductor demands.