

Experienced ASIC Analog Design Engineer (m/f/d)

工作职责

As an Experienced ASIC Analog Design Engineer (m/f/d), you will contribute to the development of analog and mixed-signal high-voltage integrated circuits for automotive applications, such as sensor interfaces and smart power devices. You will take ownership of complex transistor-level analog designs, support analog-on-top and chip-level integration, break down requirements into block-level design targets, and mentor junior engineers. Your work will contribute to the safety, efficiency, and innovation of next-generation automotive systems.

Your Tasks:

- Responsibility for the transistor-level design of complex analog and mixed-signal circuits in coordination with other designers
- Defining and implementing analog blocks in BCD technologies, such as SMPs, LDOs, gate drivers, charge pumps, sensor interfaces, and protection circuits
- Breaking down system-level requirements into block-level design targets and support specification clarification
- Discussing and investigating chip requirements with internal customers and align technical assumptions with stakeholders
- Collaborating with digital design, layout, verification, and test teams to support smooth chip integration
- Driving chip-level and analog-on-top integration activities, including schematic consistency, simulations, and interface alignment
- Mentoring junior engineers and contributing to knowledge sharing within the analog design team

职位要求

- Academic degree in electrical engineering, microelectronics, physics or comparable qualification
- Expert level of experience in analog/mixed-signal high-voltage, high-power ASIC design in the automotive domain
- Longterm Experience with analog-on-top simulation environments and mixed-signal verification
- Strong Knowledge of BCD technologies
- Proven track record of tape-outs and successful silicon validation
- Knowledge of functional safety and reliability engineering
- Exposure to EMC/ESD design and robustness testing
- Strong understanding of analog circuit theory, and physical layout considerations
- Familiarity with automotive-grade design constraints and qualification processes
- Very good English language skills (written and spoken)

Applications from severely handicapped people are welcome.



职位号码

REF10559G

工作职能

工程

所在地

Regensburg

领导力级别

个人贡献者

招聘专员

Sarah Schwarz

法律实体名称

AUMOVIO Germany GmbH

我们可以提供

- Next to the interesting daily work the region around Regensburg is offering a high recreational value and quality of life.
- Profit from extensive benefits at the location such as different canteens and cafeterias, a continuing education and training, health management, physiotherapy and much more.
- We offer an attractive salary as well as the possibility of hybrid and cross-border mobile work.
- For an improved work-life balance we support you with various initiatives for a better compatibility of family and career such as flexible work time model, part-time option, sabbatical, etc.
- You can expect a highly motivated team as well as diverse topics and projects in the global environment of the automotive industry.
- We are promoting lifelong Learning combined with personal and professional development.
- You are looking for an individual working time model? We support part-time work.

You would like to learn more about our additional services? Click [here](#).

Ready to take your career to the next level? The future of mobility isn't just anyone's job. Make it yours! **Join AUMOVIO. Own What's Next.**

关于我们

At AUMOVIO, we own what we do: Developing and producing hardware, software, and services that shape the future of mobility. But what truly sets us apart are our people. People who thrive in our winning culture, driven by bold ideas. People who take a leap and create meaningful difference—together. Because we believe that the future belongs to those who own it. That's why we foster a culture where you own every step of the way.

Own your career, your growth, your skills, and your impact. Join us and own what's next.

Advanced Electronics & Semiconductor Solutions (AESS) organization has been established in AUMOVIO to develop and manufacture semiconductors in a fabless model to fulfilling strategic internal semiconductor demands.